



Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Complete if Known	
				Application Number	10/651,376
Sheet 1 of 1				Filing Date	August 29, 2003
				First Named Inventor	Shubhendu S. Mukherjee
				Art Unit	Not Assigned
				Examiner Name	Not Assigned
				Attorney Docket Number	42P15452

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.†	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T*
CG	1	HAITHAM AKKARY, MICHAEL A. DRISCOLL, "A Dynamic Multithreading Processor," Proceedings of the 31st Annual International Symposium on Microarchitecture, Nov. 30-Dec. 2, 1998, pp. 1-11, Dallas, Texas, USA.	
CE	2	SEON WOOK KIM, ET AL., "Reference Idempotency Analysis: A Framework for Optimizing Speculative Execution," Proceedings of the SIGPLAN Symposium on Principles and Practice of Parallel Programming (PPoPP), 18-20 June 2001, pp. 1-10, Snowbird, Utah, USA.	
CB	3	DEBORAH T. MARR, ET AL., "Hyper-Threading Technology Architecture and Microarchitecture," Intel Technology Journal Q1, 2002, pp. 1-12.	
CE	4	SHUBHENDU S. MUKHERJEE, ET AL., "Detailed Design and Evaluation of Redundant Multithreading Alternatives," 29th Annual International Symposium on Computer Architecture (ISCA), 2002, pp. 1-12.	
CB	5	STEVEN K. REINHARDT, SHUBHENDU S. MUKHERJEE, "Transient Fault Detection via Simultaneous Multithreading," 27th Annual International Symposium on Computer Architecture, June 2000, pp. 1-12.	

Examiner Signature		Date Considered	3/28/06
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*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

†Applicant's unique citation designation number. *Applicant is to place a check mark here if English language Translation is attached.